

PMS150G Writing Guide

Applied for: PMS150G

1. Preface

When using semi-automatic handler to write PMS150G, the writing process may be affected by semi-automatic handler or other noise interference in the working environment, then the writing yield may fall. Moreover, the IC may be damaged. This APN provides countermeasure for improvement.

2. Problem description and the improvement tactics

Take PMS150G-S08 as an example. In order to improve the stability and reliability of writing IC, please concatenate a 100Ω resistance to the PA5 of JP2 on the back of the Writer before writing. Please refer to Figure 1 for connection method.

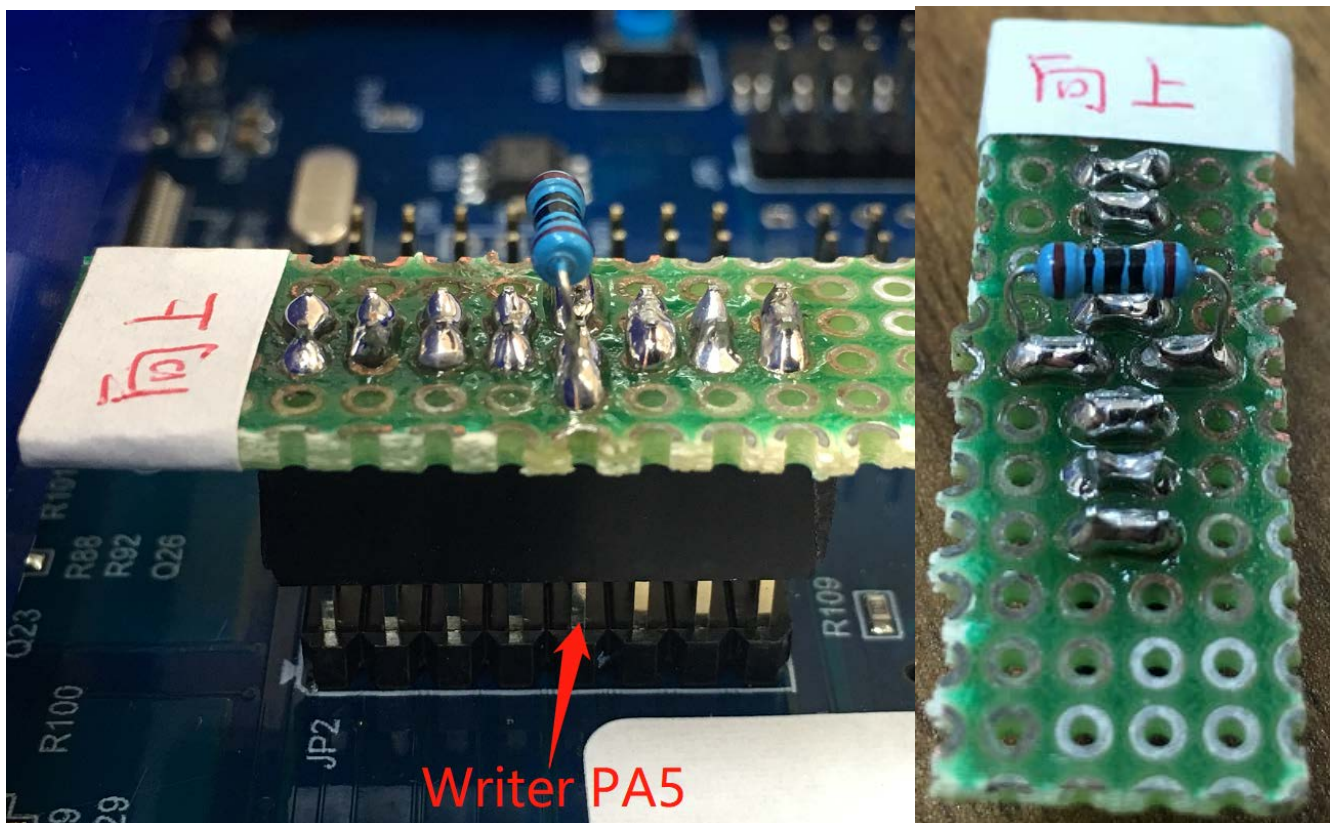


Fig.1

PMC-APN016

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If writing PMS150G-U06, please concatenate a 100Ω resistance to the PA5 pin of JP7. The connection method is shown in Figure2:

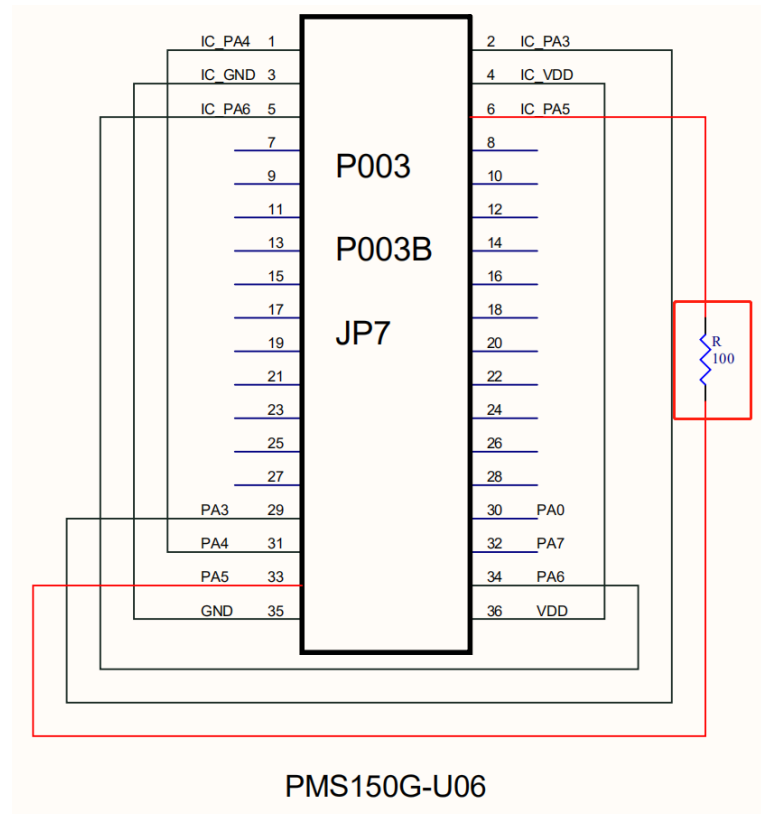


Fig.2

3. Applicable type of the writer

Both PDK5S-P-003 and PDK5S-P-003B must concatenate resistance according to the above method.

If you have further questions to the application, please consult to our agent at your nearest location or contact us at fae@padauk.com.tw.